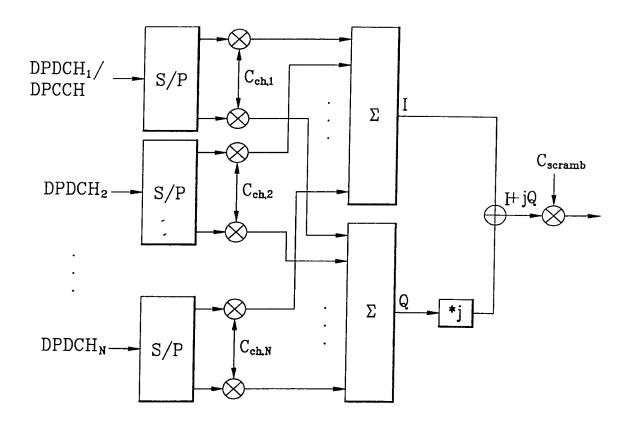
Fig. 1 Related Art



cos(at) -sin(wt) sample rate operation — Fig. 2 Related Art Over-sampling (I:L) chip rate operation \sim Output of spreader on PSC Output of spreader on the 2nd SSC Output of spreader on the 1st SSC

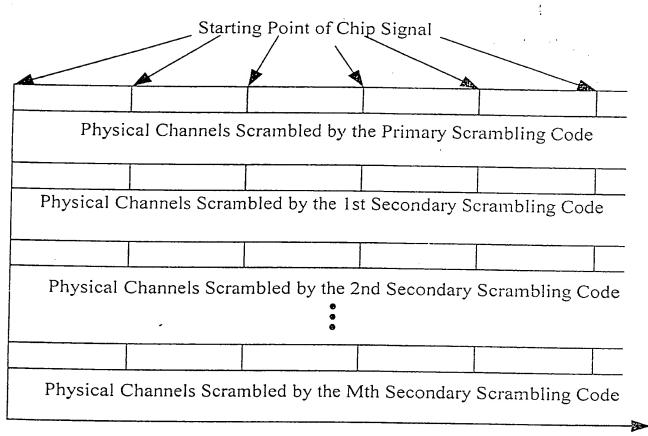
To power amplifièr

Imag

Output of spreader

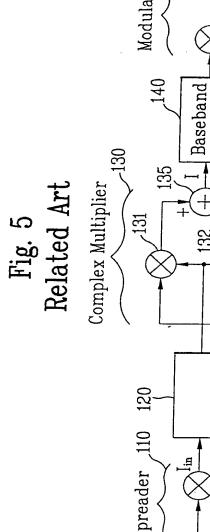
on the Mth SSC

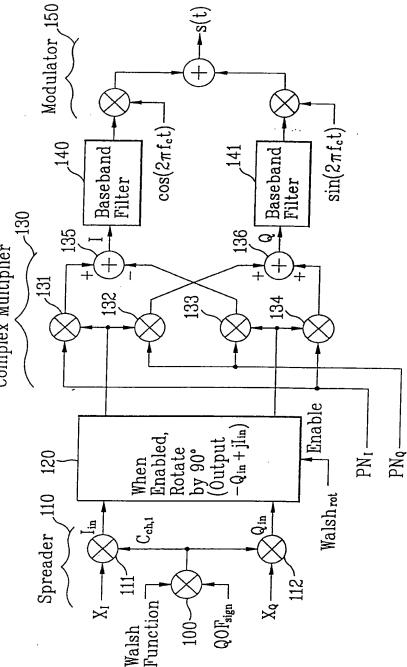
Fig. 3
Related Art



time

despreader To Under Sampling (L:1) Fig. 4 Related Art A/D | p[n] | *j A/D sin (wt) cos (ωt)





QOF $_{sign} = \pm 1$ QOF Sign Multiplier Function with the binary symbol mapping +1 for '0' and -1 for '1'. Walsh rot = 0 or 1 Walsh Function to enable 90° rotation (1 for a rotation, 0 otherwise). PN_1 and $PN_Q = \pm 1$ I-Channel and Q-Channel PN sequences. The NULL QOF has QOF $_{sign}$ = +1 and Walsh $_{rot}$ = 0.

Fig. 6 Related Art

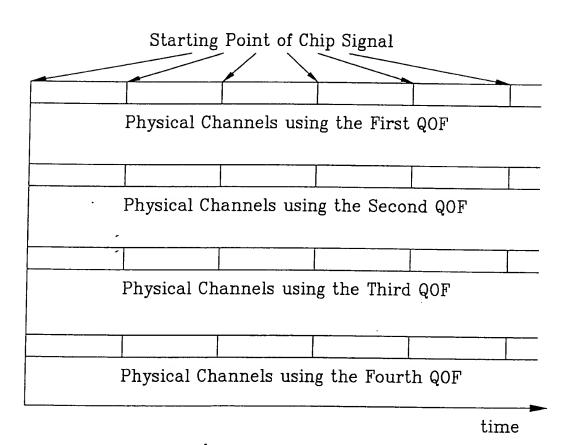


Fig. 7 Related Art

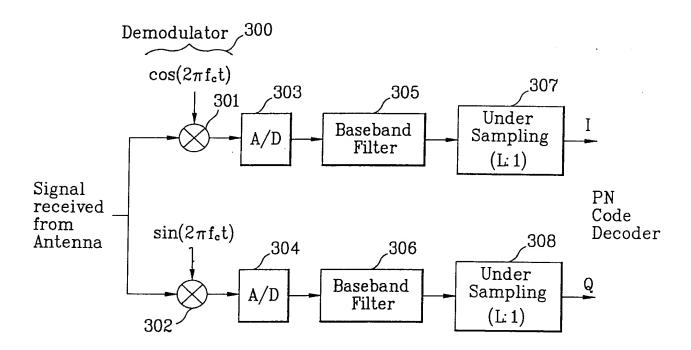
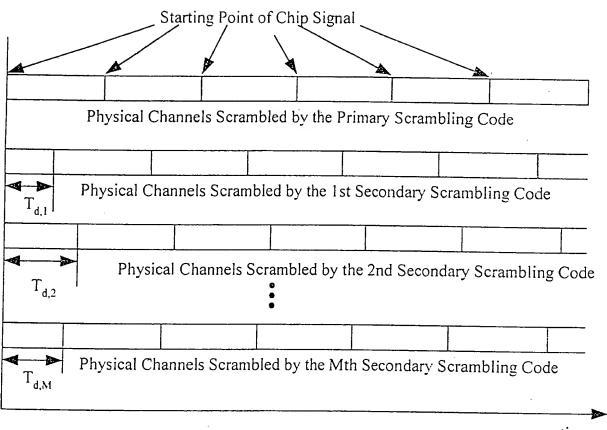


Fig. 8

Fig. 9



time

Fig. 10

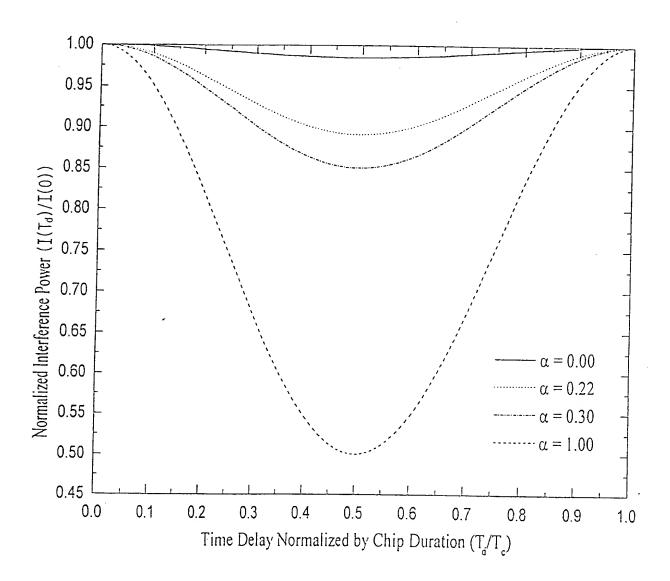


Fig. 11

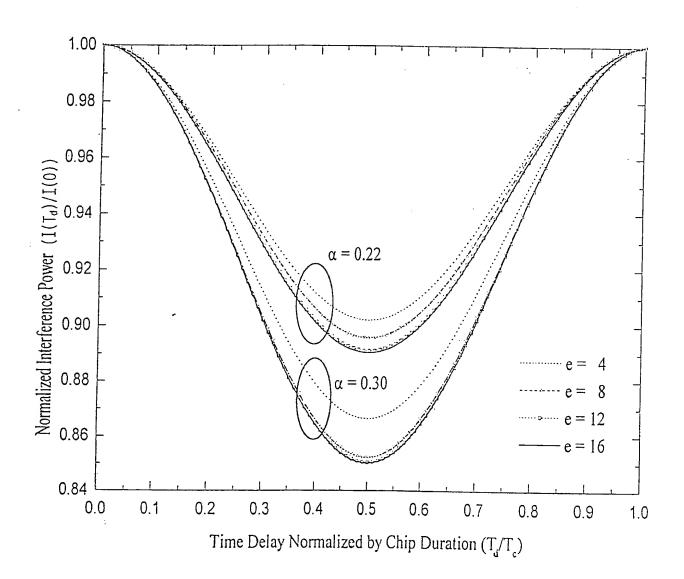
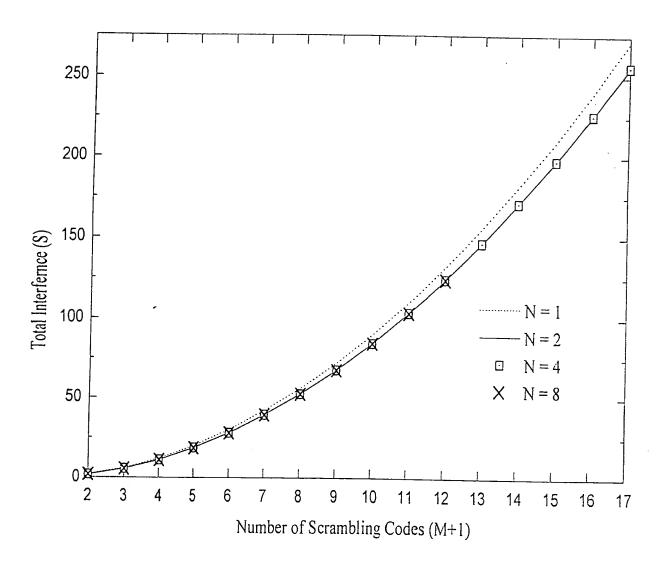
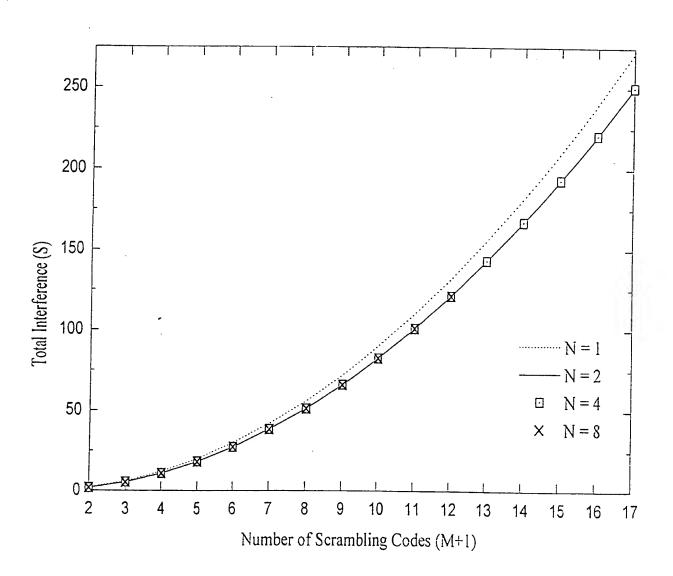
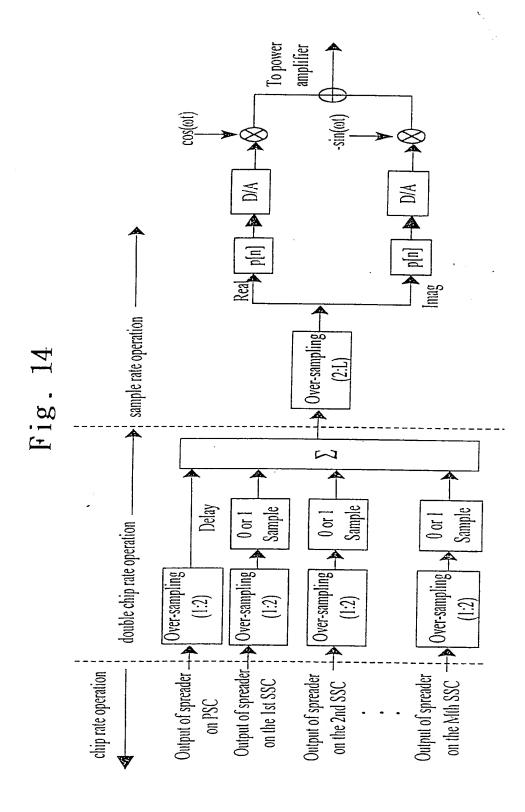


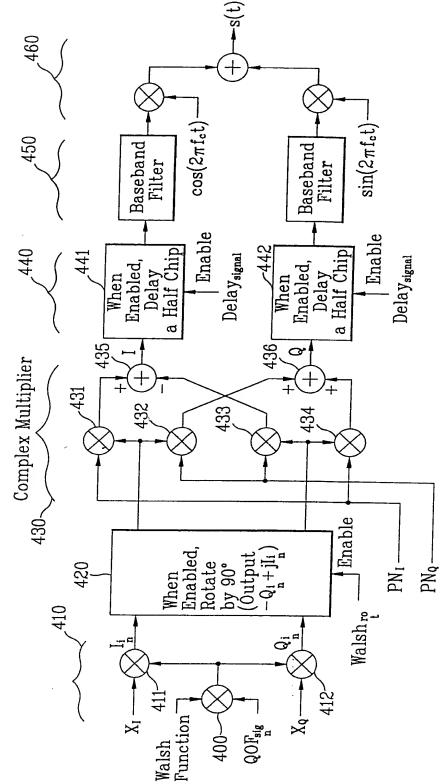
Fig. 12











 QOF_s ig = ±1 QOF Sign Multiplier Function with the binary symbol mapping +1 for '0' and -1 for '1'. ⁿWalsh $r_0 = 0$ or 1 Walsh Function to enable 90° rotation (1 for a rotation, 0 otherwise). The NULL QOF has QOF sig = +1 and Walsh ro = 0.

Fig. 17

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